

Docket No.: MUH-12796

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : MARIE DENISON ET AL.

Filed : CONCURRENTLY HEREWITH

Title : QUASI-VERTICAL SEMICONDUCTOR COMPONENT

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

Murari, B. et al. (Eds.): "Smart Power ICs – Technologies and Applications", Springer Verlag, p. 20, pp. 20 and 32-36.

Respectfully submitted,

For Applicants

Date: September 22, 2003

LAURENCE A. GREENBERG
REG. NO. 29,308

Lerner and Greenberg, P.A.
Post Office Box 2480
Hollywood, FL 33022-2480
Tel: (954) 925-1100
Fax: (954) 925-1101

/nt/kf

FORM PTO-1449 (SUBSTITUTE) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))				Attorney Docket No.: MUH-12796 Appl. No.: Applicant: MARIE DENISON ET AL. Filing Date: September 22, 2003 Group Art Unit:																																																																																			
<table border="1"> <thead> <tr> <th>EXAMINER INITIALS</th> <th></th> <th>PATENT NO.</th> <th>DATE</th> <th>PATENTEE</th> <th>CLASS</th> <th>SUB CLASS</th> <th>FILING DATE</th> </tr> </thead> <tbody> <tr><td></td><td>A</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td>B</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td>C</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td>D</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td>E</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td>F</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td>G</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td>H</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td>I</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </tbody> </table>								EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE		A								B								C								D								E								F								G								H								I						
EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE																																																																																
	A																																																																																						
	B																																																																																						
	C																																																																																						
	D																																																																																						
	E																																																																																						
	F																																																																																						
	G																																																																																						
	H																																																																																						
	I																																																																																						
FOREIGN PATENT DOCUMENT																																																																																							
<table border="1"> <thead> <tr> <th></th> <th></th> <th>DOCUMENT NO.</th> <th>DATE</th> <th>COUNTRY</th> <th>CLASS</th> <th>SUB CLASS</th> <th>TRANSL. YES NO</th> </tr> </thead> <tbody> <tr><td></td><td>J</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td>K</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td>L</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td>M</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td>N</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </tbody> </table>										DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO		J								K								L								M								N																																						
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES NO																																																																																
	J																																																																																						
	K																																																																																						
	L																																																																																						
	M																																																																																						
	N																																																																																						
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)																																																																																							
<table border="1"> <tr><td></td><td></td><td colspan="6">Murari, B. et al. (Eds.): "Smart Power ICs – Technologies and Applications", Springer Verlag, p. 20, pp. 20 and 32-36</td></tr> <tr><td></td><td></td><td colspan="6"></td></tr> </table>										Murari, B. et al. (Eds.): "Smart Power ICs – Technologies and Applications", Springer Verlag, p. 20, pp. 20 and 32-36																																																																													
		Murari, B. et al. (Eds.): "Smart Power ICs – Technologies and Applications", Springer Verlag, p. 20, pp. 20 and 32-36																																																																																					
EXAMINER				DATE CONSIDERED																																																																																			
<table border="1"> <tr><td colspan="8"></td></tr> </table>																																																																																							